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		Application Number	09/824,653		_)	
TRANSMITTAL		Filing Date	April 3, 2001			
FORM		First Named Inventor	Terry Lines			
(to be used for all correspondence after initial filing)		Group Art Unit	2823			
		Examiner Name	D. Collins	. Collins		
otal Number of Pages in This Submissi	on 2	Attorney Docket Number	100-14300	(P04927)		
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Law Offices of Mark C. Pickering P.O. Box 300 Petaluma, CA 94953-0300

Patent Appin. No. 09/824, 653	epletion-Mode Transistor
In the Matter of the Application of: To well Indis	سمان س
Tisles	- lation-Mode Transistor
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Transmittal Letter	Request For:
Perent Application	Status Inquiry
ngs specification including (Claims and Abstract)	Certificate of Mailing
Drawings sheets formalinformal	Express Mail Certificate No
Oath/Declaration	Certificate under §3.73(b)
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Patent Appin. No. 09/824, 653 In the Matter of the Application of: Terry Lines Title: Method for Forming a D Date Maried: 07-23-02 The following has been received in the U.S. Patent and Trademark O	File No. 100 - 14300 By: MCP
In the Matter of the Application of: Terry Lines	1 1 1 TIPE LAN
Method for Forming a D	epletion-Mode Transistor
Date Mailed: 07-23-02	Due Date:
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Patent Application	Status Inquiry PRADENARY
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Power of Attorney	Issue Fee Transmittal
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